## In the Specification

Please replace paragraph 8 with the following rewritten paragraph:

[0008] In one embodiment, an apparatus includes a regulator having a first amplifier with a first input terminal coupled to a reference terminal of the regulator to receive a reference voltage. The regulator also includes a second amplifier that has a first terminal coupled to the output terminal of the first amplifier. The second amplifier has a second terminal that receives the input voltage and a third terminal that is coupled to the regulator's output terminal. The second amplifier's third terminal is also coupled to a second input terminal of the first amplifier. The regulator further includes a variable capacitor that is disposed between the output terminal of the first amplifier and the third second terminal of the second amplifier. In one embodiment, the variable capacitor may be a transistor with its gate coupled to the output terminal of the first amplifier and the drain and source both coupled to the second first terminal of the second amplifier. There may be a transistor disposed between the second third terminal of the second amplifier and the variable capacitor. In another embodiment, a plurality of regulators has output terminals coupled together and are all included on a single integrated circuit.

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